

INTEFERENCE SEARCH

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|-----------------|------------------|---------|------------------|
| L1 | 3 | ("OCDS" and (power adj3 supply)).clm. | US-PGPUB; USPAT | OR | ON | 2006/12/06 17:13 |
| L2 | 0 | (debugger and (plurality with (voltage adj supplies))).clm. | US-PGPUB; USPAT | OR | ON | 2006/12/06 17:14 |
| L3 | 1 | ("OCDS" and (control adj information adj memory)).clm. | US-PGPUB; USPAT | OR | ON | 2006/12/06 17:14 |
| L4 | 169 | ("OCDS").clm. | US-PGPUB; USPAT | OR | ON | 2006/12/06 17:14 |
| L6 | 5 | "714"/\$.ccls. and ("OCDS").clm. | US-PGPUB; USPAT | OR | ON | 2006/12/06 17:15 |
| L7 | 0 | (debugger and (voltage adj supply)).clm. | US-PGPUB; USPAT | OR | ON | 2006/12/06 17:16 |
| L8 | 1 | ("OCDS" and (control adj information adj memory)).clm. | US-PGPUB; USPAT | OR | ON | 2006/12/06 17:16 |
| L9 | 8 | ("on-chip" adj debug\$3 adj support).clm. | US-PGPUB; USPAT | OR | ON | 2006/12/06 17:16 |

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|------------------|
| S1 | 65 | "OCDS" | USPAT | OR | OFF | 2006/04/05 11:00 |
| S2 | 639 | 714/30.ccls. | USPAT | OR | OFF | 2006/04/04 15:31 |
| S3 | 252 | 714/31.ccls. | USPAT | OR | OFF | 2006/04/04 15:31 |
| S4 | 911 | 714/29-31.ccls. | USPAT | OR | OFF | 2006/09/17 15:58 |
| S5 | 187 | "OCDS" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/04 16:04 |
| S6 | 70 | "OCDS" and voltage | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/05 14:57 |
| S7 | 0 | S4 and "OCDS" and voltage | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/04 16:05 |
| S8 | 208 | 714/29-31.ccls. and voltage | USPAT | OR | OFF | 2006/04/05 15:48 |
| S9 | 19 | 714/29-31.ccls. and (second\$3 adj3 voltage) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/04 16:17 |
| S10 | 469 | debug\$4 and (second\$3 adj3 voltage) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/06 17:12 |
| S11 | 0 | ((second\$3 adj3 voltage) adj4 debug\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/04 16:15 |
| S12 | 5 | 714/29-31.ccls. and (second\$3 adj3 power adj supply) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/04 16:17 |

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| S14 | 0 | "10/670,664" and actuation | USPAT | OR | OFF | 2006/04/05 13:44 |
| S15 | 1 | "10/670,664" and actuation | US-PGPUB; USPAT | OR | OFF | 2006/04/05 13:44 |
| S19 | 7 | ("20020147939" "20030074180" "5226047" "5283905" "5935266" "6044476" "6339753").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/04/05 14:53 |
| S21 | 45 | "OCDS" and Debug\$5 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/05 15:35 |
| S22 | 1 | "6922794".pn. and user adj program | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/05 15:38 |
| S23 | 1 | "6922794".pn. and program | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/04/05 15:38 |
| S24 | 60 | 714/29-31.ccls. and debug\$4 and (control adj information) | USPAT | OR | OFF | 2006/04/05 15:50 |
| S25 | 3 | OCDS and (control adj (information memory)) | USPAT | OR | OFF | 2006/04/05 15:49 |
| S26 | 15 | 714/29-31.ccls. and debug\$4 with (control adj (memory information)) | USPAT | OR | OFF | 2006/04/05 16:21 |
| S27 | 1 | "6922794".pn. and trace | USPAT | OR | OFF | 2006/04/05 16:50 |
| S30 | 1 | "6922794".pn. and terminals | USPAT | OR | OFF | 2006/04/05 17:13 |
| S31 | 1596 | debug\$4 same memory same external | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/05 17:15 |
| S32 | 31 | (external adj debug\$4) same (memory with program) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/17 16:13 |

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| S33 | 3 | (external adj debug\$4) same (memory with (debug\$4 adj5 support)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/05 17:19 |
| S34 | 10 | 714/29-31.ccls. and (memory with (debug\$4 adj5 support)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/05 17:24 |
| S35 | 1 | 714/29-31.ccls. and ((debug\$4 adj5 support) adj5 (memory RAM)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/05 17:25 |
| S36 | 3 | 714/29-31.ccls. and ((debug\$4 adj5 support) with stor\$3 adj (program data information)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/05 17:28 |
| S37 | 14 | ((debug\$4 adj5 support) with stor\$3 adj (program data information)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/05 17:33 |
| S38 | 19 | debug\$4 same (stor\$3 adj (program data information) adj7 external) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/05 17:42 |
| S39 | 90 | puente.xa. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/05 17:41 |
| S40 | 1 | puente.xa. and boot adj rom | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/05 17:41 |

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| S41 | 31 | debug\$4 same (transmit\$4 adj (program data information) adj7 external) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/05 18:01 |
| S42 | 635 | debug\$4 same ((program data information) adj3 (cache memory storage))same external | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/17 16:09 |
| S56 | 2 | ((debug\$4 adj support) with (cache buffer RAM storage ROM) with (stor\$4 adj program)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/17 16:10 |
| S57 | 0 | (buffer with receiv\$3 with (data program information) with external with debugger) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/06 13:27 |
| S58 | 4076 | (buffer with receiv\$3 with (data program information) with external) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/06 13:27 |
| S59 | 8 | 714/29-31.ccls. and (buffer with receiv\$3 with (data program information) with external) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/06 13:29 |
| S60 | 163 | 714/29-31.ccls. and emulator | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/06 13:30 |
| S61 | 39 | 714/29-31.ccls. and debug\$4 adj support | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/06 13:46 |

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| S67 | 92 | (debug\$4 adj support) and (control adj (device memory)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/06 17:17 |
| S68 | 28 | (debug\$4 and (OCDS adj module)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/06 17:18 |
| S69 | 11 | (debug\$4 and (OCDS adj module)) and (control adj (device memory)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/04/06 17:18 |
| S70 | 69 | 714/29-31.ccls. and @pd>"20060405" | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/06 17:04 |
| S71 | 223 | @pd>"20060405" and "OCDS" | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/17 16:05 |
| S72 | 141 | @pd>"20060405" and "OCDS" and memory | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/06 17:04 |
| S73 | 3 | "714"/\$.ccls. and @pd>"20060405" and "OCDS" | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/06 17:04 |

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| S74 | 0 | @pd>"20060405" and ((debug\$4 adj support) with (cache buffer RAM storage ROM) with (stor\$4 adj program)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/17 16:10 |
| S75 | 18 | "OCDS" and (control adj memory) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/17 16:11 |
| S76 | 4 | @pd>"20060405" and (external adj debug\$4) same (memory with program) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/06 17:05 |
| S88 | 306921 | @pd>"20060917" | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/06 17:04 |
| S89 | 48 | 714/29-31.ccls. and S88 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/06 17:08 |
| S90 | 102 | "OCDS" and S88 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/06 17:04 |
| S91 | 54 | "OCDS" and memory and S88 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/06 17:08 |
| S92 | 6 | (debug\$3 adj support) and S88 | US-PGPUB; USPAT | OR | ON | 2006/12/06 17:09 |
| S93 | 0 | ("on-chip" adj debug\$3 adj support) and S88 | US-PGPUB; USPAT | OR | ON | 2006/12/06 17:09 |

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| S94 | 0 | (external adj debug\$4) same (memory with program) and S88 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/06 17:09 |
|-----|---|---|---|----|----|------------------|